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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/525,244	02/22/2005	Ryoji Hoshi	122810	5373
25944 7590 03/05/2007 OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			EXAMINER RAO, G NAGESH	
			ART UNIT 1722	PAPER NUMBER
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		03/05/2007	PAPER	

**Please find below and/or attached an Office communication concerning this application or proceeding.**

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

<b>Office Action Summary</b>	Application No. 10/525,244	Applicant(s) HOSHI ET AL.	
	Examiner G. Nagesh Rao	Art Unit 1722.	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 13-32 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 13-32 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |  |
|--|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                 | 5) <input type="checkbox"/> Notice of Informal Patent Application                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date ____ | 6) <input type="checkbox"/> Other: ____  |

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1) Claims 13-21 and 23 are rejected under 35 U.S.C. 102(b) as being anticipated by Fusegawa (US Patent No. 6,117,231).

Examiner would like to point out that claims 13-21 and 23 are being treated as Product by Process claims and according to MPEP 2113 [R-1]

“[E]ven though product-by-process claims are limited by and defined by the process, determination of patentability is based on the product itself. The patentability of a product does not depend on its method of production. If the product in the product-by-process claim is the same as or obvious from a product of the prior art, the claim is unpatentable even though the prior product was made by a different process.” In re Thorpe, 777 F.2d 695, 698, 227 USPQ 964, 966 (Fed. Cir. 1985) (citations omitted) (Claim was directed to a novolac color developer. The process of making the developer was allowed. The difference between the inventive process and the prior art was the addition of metal oxide and carboxylic acid as separate ingredients instead of adding the more expensive pre-

reacted metal carboxylate. The product-by-process claim was rejected because the end product, in both the prior art and the allowed process, ends up containing metal carboxylate. The fact that the metal carboxylate is not directly added, but is instead produced in-situ does not change the end product.). The structure implied by the process steps should be considered when assessing the patentability of product-by-process claims over the prior art, especially where the product can only be defined by the process steps by which the product is made, or where the manufacturing process steps would be expected to impart distinctive structural characteristics to the final product. See, e.g., *In re Garnero*, 412 F.2d 276, 279, 162 USPQ 221, 223 (CCPA 1979) (holding “interbonded by interfusion” to limit structure of the claimed composite and noting that terms such as “welded,” “intermixed,” “ground in place,” “press fitted,” and “etched” are capable of construction as structural limitations.)”

Fusegawa 231 teaches a Si (silicon) single crystal having a resistivity ( $\rho$ ) value range of 0.005 to 0.2  $\Omega \cdot \text{cm}$  whereby applicants claimed 0.1  $\Omega \cdot \text{cm}$  falls within claimed range, as well in manufacturing example 1 the Si single crystal was grown with a diameter of 200mm. Furthermore Fusegawa 231 teaches furthermore that the resulting silicon single crystal is shaped into a Si single crystal substrate, noted as having been the wafer sliced from the Si single crystal, where on which a

Si single crystal is epitaxially grown (See Abstract and Col. 2-Col. 7). Examiner notes the interval of striations and range controlled with respect to the Si single crystal are viewed as process limitations and bear no consideration to the attributes of the Si single crystal product as claimed by applicant.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any

inventions covered therein were made absent any evidence to the contrary.

Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

2) Claims 22 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fusegawa (US Patent No. 6,117,231) in view of Pietsch (US Patent No. 7,077,726).

From the aforementioned rejection Fusegawa 231 pertains to teaching a Si single crystal and its method of manufacturing.

However Fusegawa 231 fails to address the crystalline product material and the epitaxial layer grown on a wafer cut from the crystal in relation to the specific nanotopology/nanotopography nature of the product.

However in analogous art, Pietsch 726 pertains to a semiconductor wafer with improved local flatness and the method of its production in particular to the nanotopology/nanotopography level of the wafer. Pietsch 726 discloses within range of having the nanotopology/nanotopography range be within the 14nm range

as claimed by applicant in a 2mm x 2mm square (See Claim 2, Col. 2 Lines 1-68, Examples 1-2, Col. 14 Line 60 – Col. 17 Line 8).

Therefore it would be obvious to one having ordinary skill in the art at the time of the invention to incorporate the teachings of Pietsch 726 to that of Fusegawa 231 to ensure that the flatness of the wafer is flat at the nanoscopic scale in order to improve upon creating nano-meter based transistors and devices on the wafers in order to fully exploit the expected potentials of the nanotechnology development.

### *Claim Rejections - 35 USC § 103*

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.

3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.



3) Claims 13-21, 23, and 25-32 are rejected under 35 U.S.C. 102(b) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Kishida (US Patent No. 6,200,384) in view of Fusegawa (US Patent No. 6,117,231).

Examiner would like to point out that claims 13-24 are being treated as Product by Process claims and according to MPEP 2113 [R-1]

“[E]ven though product-by-process claims are limited by and defined by the process, determination of patentability is based on the product itself. The patentability of a product does not depend on its method of production. If the product in the product-by-process claim is the same as or obvious from a product of the prior art, the claim is unpatentable even though the prior product was made by a different process.” *In re Thorpe*, 777 F.2d 695, 698, 227 USPQ 964, 966 (Fed. Cir. 1985) (citations omitted) (Claim was directed to a novolac color developer. The process of making the developer was allowed. The difference between the inventive process and the prior art was the addition of metal oxide and carboxylic acid as separate ingredients instead of adding the more expensive pre-reacted metal carboxylate. The product-by-process claim was rejected because the end product, in both the prior art and the allowed process, ends up containing metal carboxylate. The fact that the metal carboxylate is not directly added, but is instead

produced in-situ does not change the end product.). The structure implied by the process steps should be considered when assessing the patentability of product-by-process claims over the prior art, especially where the product can only be defined by the process steps by which the product is made, or where the manufacturing process steps would be expected to impart distinctive structural characteristics to the final product. See, e.g., *In re Garner*, 412 F.2d 276, 279, 162 USPQ 221, 223 (CCPA 1979) (holding “interbonded by interfusion” to limit structure of the claimed composite and noting that terms such as “welded,” “intermixed,” “ground in place,” “press fitted,” and “etched” are capable of construction as structural limitations.)”

Kishida 384 pertains to the method of producing silicon (Si) single crystals whereby the single crystal produced is obtained through a controlled pulling method means via the application of a magnetic field which helps control the temperature fluctuation of the silicon melt, and in turn results in controlling the interval of striations incorporated into the single crystal (This phenomenon can be attributed as being known by applicant’s admission of prior art in the specification). The interval of striation control range from 1.5 mm or less or 2.3 mm or more is read as a process technique step imposed on the creating the product of silicon single crystal thus it does not bear structural weight to the crystal

product. As well Kashida 384 teaches slicing off a wafer from the crystal grown in the referenced teaching.

Furthermore the resistivity value of the silicon single crystal is a dynamic and not a static value, that is to say that it is known that resistivity values for materials are the inverse of the inherent conductivity ( $\sigma$ ) value of the material but also resistivity ( $\rho$ ) is temperature dependent because conductivity is by the mathematical equation  $\sigma = ne\mu$  where  $n$  means carrier concentration (No. of electrons),  $e$  is the absolute charge on the electron, and  $\mu$  is the mobility value, whereby  $\mu = eD/kT$ , where  $T$  in the equation is temperature value, ergo  $\sigma$  value is dependent on the temperature of the material and with  $\rho$  being the inverse of  $\sigma$ , it too would be temperature dependent. Thus Si single crystal grown as a result of the teachings of Kashida 384 inherently has the capability of satisfying the value as claimed by applicant of  $0.1 \Omega \cdot \text{cm}$  based on temperature conditions put forth. Finally the diameter of the single crystal depends from the crucible it is produced from and Kashida 384 teaches the crucible utilized in producing the Si single crystal is grown out of would produce the crystal at a diameter of 200 mm or more.

Finally with respect to claims 25-32 as related to the above referenced information of Kashida 384 as it pertains to the production of the single Si crystal material, the controlling of the temperature fluctuation is controlled by a variety of


manners, most notably the magnetic field impressed to the crystal melt as to inherently it would satisfy the method of growing the material to satisfy the  $V \times F / \sin \Theta$  equation as derived by applicant essentially the distance as measure in mm divided by  $\sin \Theta$  ergo distance as divided by the function of the angle of the growth surface of the crystal growth (Please see Abstract, Fig. 2, Col 2 Lines 39-68, Col 3 – Col 10 Lines 1-68 (note Col 4 Lines 45-57, Col 6 Lines 1-31)), Tables 1 and 2, Also examiner is including in the NPL a secondary reference “Electronic Materials Science for Integrated Circuits in Si and GaAs” that teaches the thermodynamic calculations behind conductivity and resistivity calculations, please note that this secondary reference is used to teach what is inherently known and understood about calculated values for the properties of the material’s characteristic).

However in order to further substantiate the referenced teachings of Kashida 384, examiner has put forth analogous art Fusegawa 231 which pertains as well to the manufacturing of semiconductor silicon crystal wafer which is obviously cut from single crystal ingots, and Fusegawa 231 substantiates that Si single crystal falls in the  $0.1 \Omega \cdot \text{cm}$  resistivity value with teaching a range of  $0.005$  to  $0.2 \Omega \cdot \text{cm}$ . Furthermore Fusegawa 231 teaches growing the epitaxial layer of Si single crystal ontop of the single Si crystal wafer (See Abstract and Col. 2-Col. 7).

It would therefore be obvious to one having ordinary skill in the art at the time of the invention to incorporate the steps and knowledge of Fusegawa 231 into that of Kashida 384 in order to further optimize the material parameters for the production and crystal structure and strength of the Si single crystal material.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to G. Nagesh Rao whose telephone number is (571) 272-2946. The examiner can normally be reached on 9AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Yogendra Gupta can be reached on (571)272-1316. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

  
ROBERT DAVIS  
PRIMARY EXAMINER  
GROUP 1300/1722

3/1/07

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GNR